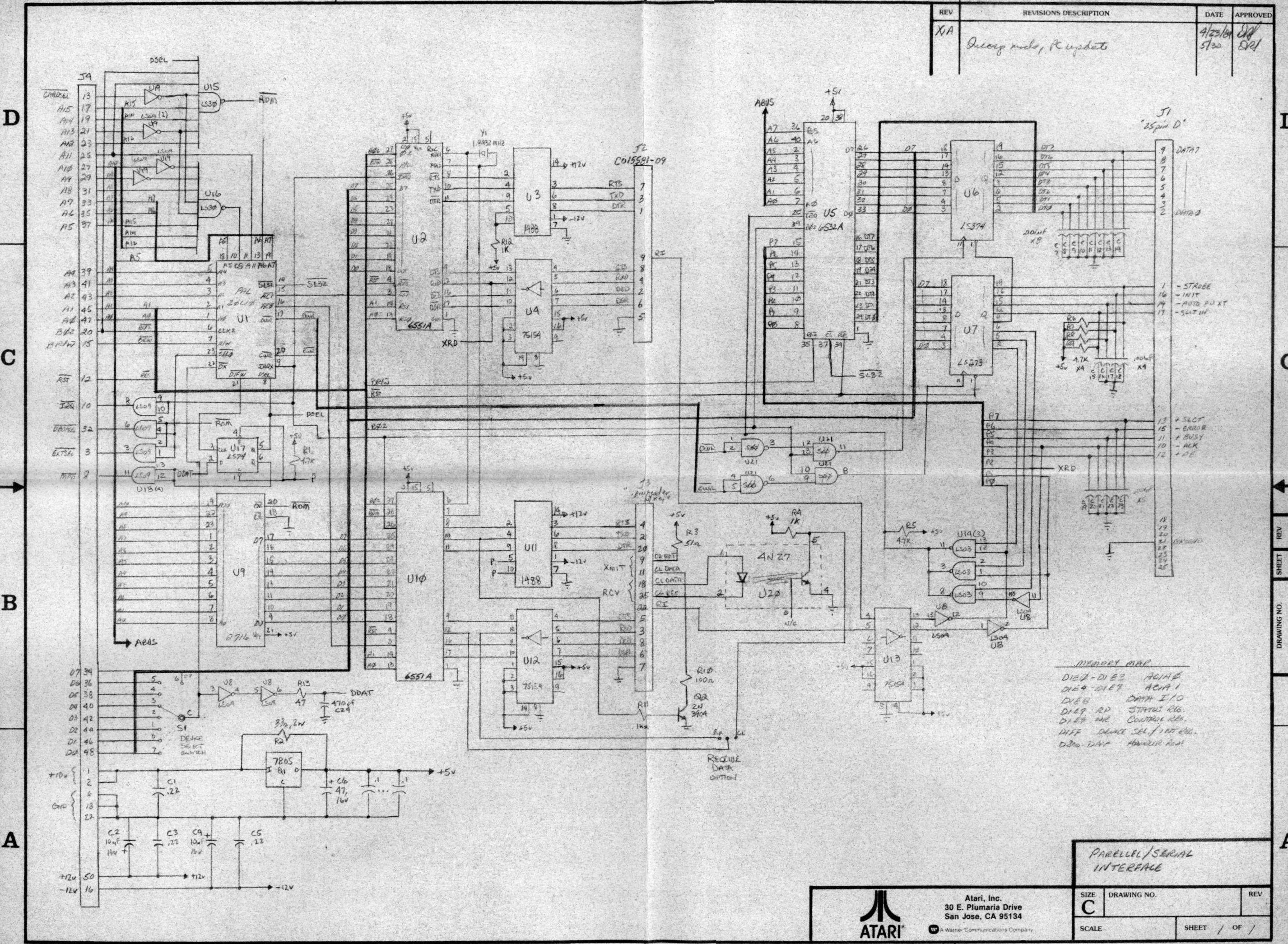


REV	REVISIONS DESCRIPTION	DATE	APPROVED
XIA	Design notes, R updates	4/23/84 5/30	[Signature]



MEMORY MAP
 DIE 1 - DIE 2 ACIA
 DIE 3 - DIE 4 ACIA 1
 DIE 5 - DATA I/O
 DIE 6 - RD STATUS REG.
 DIE 7 - CONTROL REG.
 DIE 8 - DATA SELECT / INT REG.
 DIE 9 - DATA ADDRESS REG.

PARALLEL/SERIAL INTERFACE



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SIZE	DRAWING NO.	REV
C		
SCALE	SHEET / OF /	